Allied Vision



HR 10GigE

hr51MXGE-G2

□□	
□□	hr51MXGE-G2
□□ □□	F004153
□□ □□□	HR 10GigE
□□	Deleted

□□	
□□ □□	Area scan
□□□	Mono
□□□□	Visible
□□□□ □□	400 nm to 1000 nm
□□□	8,424 × 6,032 (51.00 MP)
□□ □□	Gpixel GMAX4651-BVM-BU2
□□ □□□□ (□□)	cmos
□□ □□	Global shutter (GS)
□□ □□	38.75 × 27.75 mm (47.66 mm, 35mm Full Frame)
□□ □□	4.60 µm × 4.60 µm

□□ □□	
□□ □□ □□	8-Bit,12-Bit
□□□□ □□ □□	mono8, mono12

□□□ □□	
□□□□ □□□	65 dB
SNR	42.6 dB

□□□ □ □□

□□ □□□ □□	23.7 fps
□□ □□	60 µs (max)
□□	0.0 dB to 40.0 dB

I/O □ □□

□□□ □□	0 x LVDS input, 0 x LVDS output0 x TTL input, 0 x TTL output, 2 x 24V input, 4 x Open drain output
□□ □□□ □□	1 x RS232 input, 1 x RS232 output, 0 x RS422 input, 0 x RS422 output
□□□ □□	1 x Optical isolated input, 0 x Optical isolated Input,
□□ □□ □□	10 to 25VDC
□□ □□	External: 17 W (typical at 12 VDC)

□□□ □□

□□ □□ (L x W x H mm)	77 × 70 × 70
□□/□□ □□	N-BK7 - AR coating 380-850nm
IP □□	IP30
□□ □□□	M58x0.75
□□	380 g

□□□ □□□ □ FPGA

□□□ □□ (RAM)	448 MByte

□□□□□

□□□ □□□□□□	10gige
□□□□□ □□□	(RJ-45)

FW □□ - □□□ □□

□□ □□	Manual, Auto
□□ □□	Auto, Manual
□□□ □□ □□	FW Features - Image Control

FW □□ - □□□ □□

□□□ □□/□□□	INTERNAL,SOFTWARE,EXTERNAL
□□□ □□ □□	User Sets, POE, PWM(4), Sequencer,

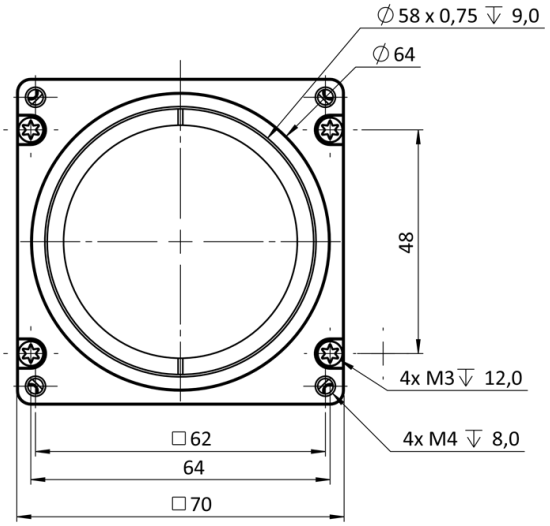
Quantum Efficiency



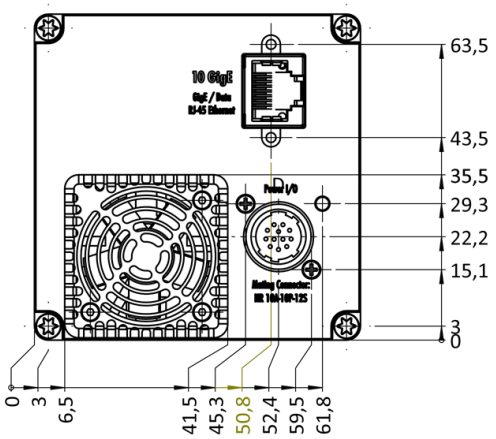
Allied Vision

Technical Drawing

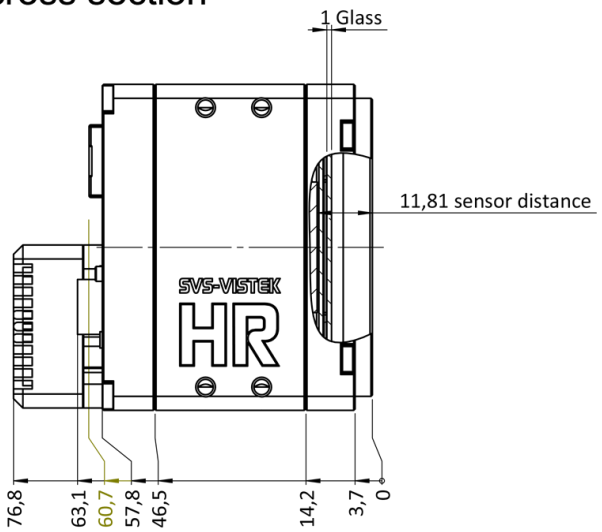
front



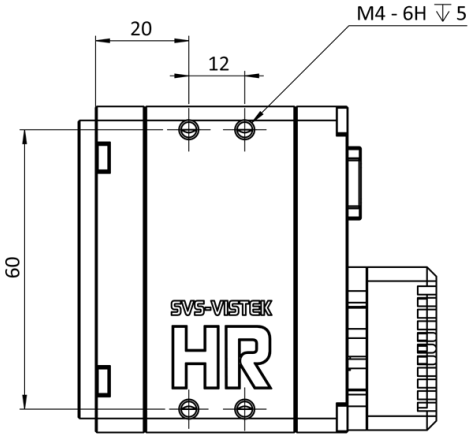
back



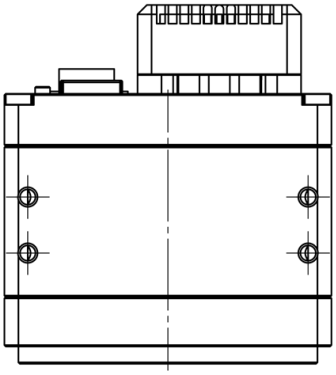
cross section



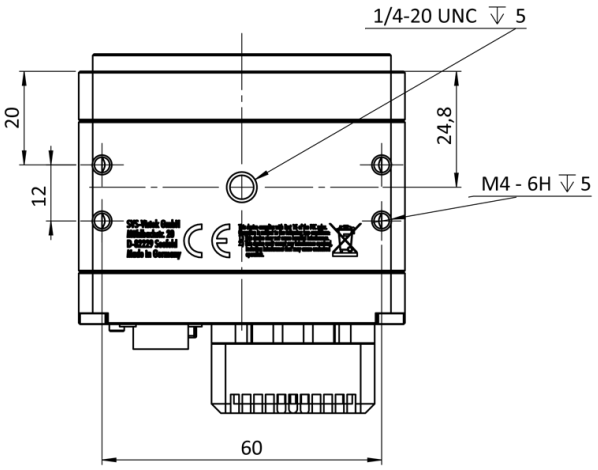
right side



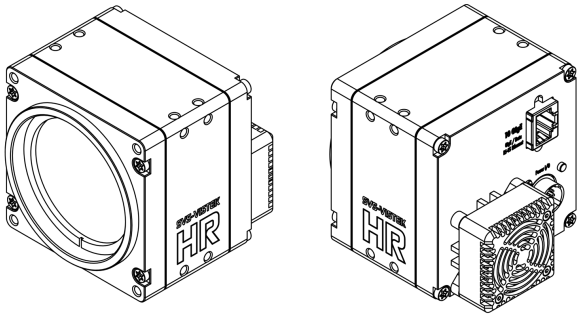
top



bottom

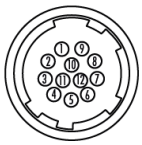


3D



I/O pin assignment

Hirose 12 Pin



1	VIN –	(GND)	7	OUT 1	(open drain)
2	VIN +	(10 V to 25 V DC)	8	OUT 2	(open drain)
3	IN 4	(RXD RS232)	9	IN 3 +	(opto In +)
4	OUT 4	(TXD RS232)	10	IN 3 –	(opto In –)
5	IN 1	(0 - 24V)	11	OUT 3	(open drain)
6	IN 2	(0 - 24V)	12	OUT 0	(open drain)