



SHR 10GigE

shr661CXGE

General

Model	shr661CXGE
Product code	F004144
Product series	SHR 10GigE
Status	Available

Sensor

Sensor type	Area scan
Chroma	Color
Spectrum	Visible
Spectral range	400 nm to 1000 nm
Resolution	13,392 × 9,528 (127.60 MP)
Sensor model	Sony IMX661
Sensor architecture (material)	cmos
Shutter type(s)	Global shutter (GS)
Sensor size	46.2 × 32.87 mm (56.7 mm, Type 3.6)
Pixel size	3.45 µm × 3.45 µm

Pixel formats

Sensor bit depth	8-bit, 12-bit
RGB pixel formats	bayer8, bayer12packed

Imaging performance

Dynamic range	74.4 dB
SNR	41.5 dB

Timing and gain

Max. frame rate	8.2 fps
Exposure time	163 μ s to 60 s
Gain	0.0 dB to 36.0 dB

I/Os and power

Non-isolated lines	0 x LVDS input, 0 x LVDS output, 0 x TTL input, 0 x TTL output, 2 x 24V input, 4 x Open drain output,
Specific non-isolated lines	1 x RS232 input, 1 x RS232 output, 0 x RS422 input, 0 x RS422 output,
Opto-isolated lines	1 x Optical isolated input, 0 x Optical isolated input,
I/O connector	12-pin Hirose HR10A-10P-12S
Power supply	10 to 25VDC, Power over Ethernet (POE+, in option -P)
Power consumption	External: 18 W (typical)

Operating conditions

Operating temperature (housing)	-10 °C to 60 °C
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Mechanical properties

Body dimensions (L x W x H in mm)	83 x 80 x 80
Filter/protection glass	IR-Cut 680
IP class	IP30
Lens mount(s)	M72x0.75
Weight	580 g

On-board memory and FPGA

Non-volatile memory (Flash)	32 MByte
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Interfaces

Digital interface	10gige
Interface connector	(RJ-45)
Vision standard	GigE Vision 2.0

FW features - image control

Exposure modes	manual, auto, external
Gain modes	auto, manual

FW features - image control

White balance modes	auto, manual
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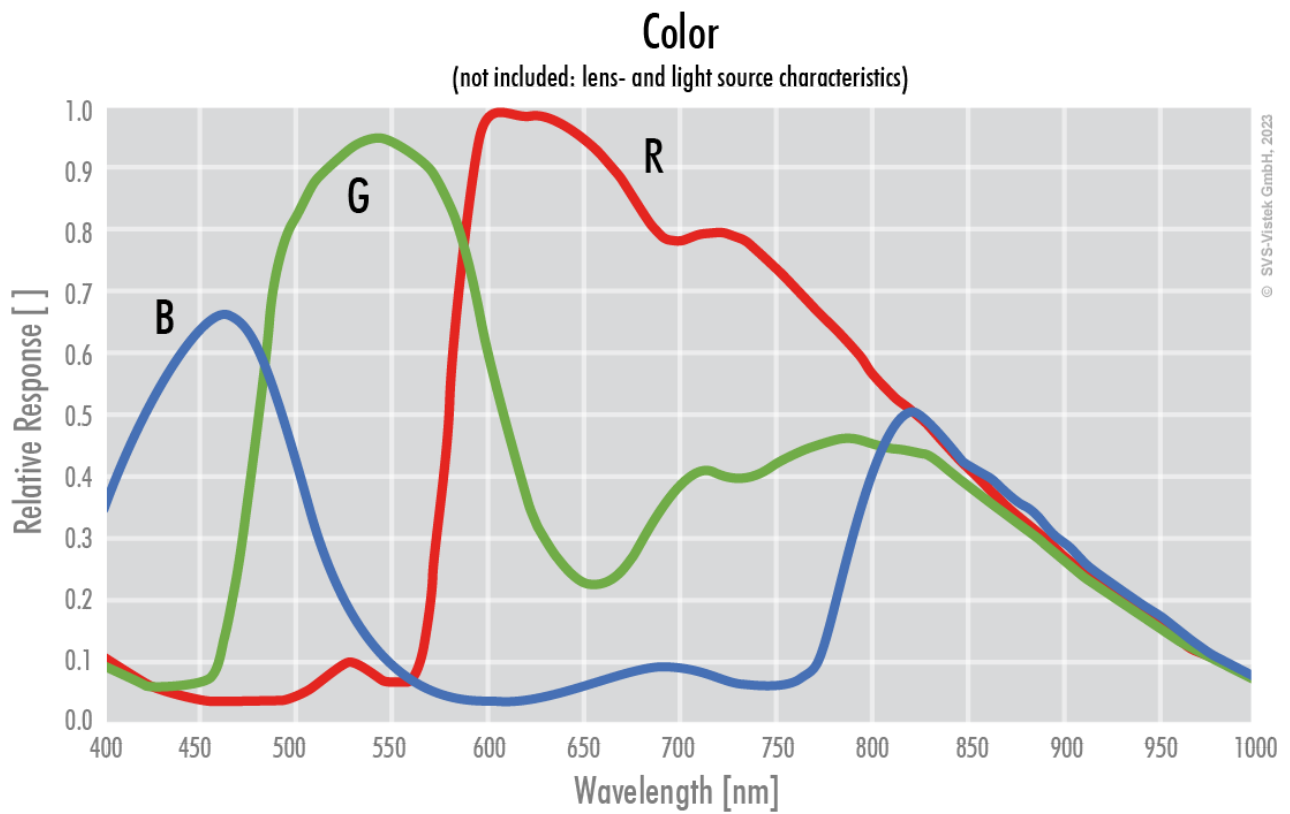
Image control features	lut, mirror-image, dpc, shading-correction, ffc
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FW features - camera control

Trigger modes/sync	internal, software, external
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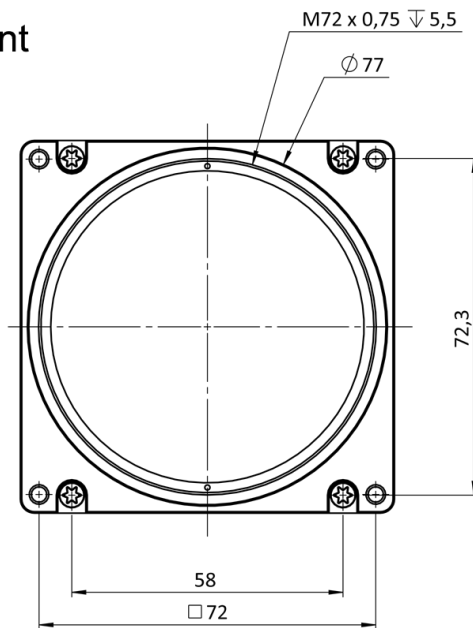
Camera control features	User Sets, POE, PWM(4), Sequencer,
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Quantum Efficiency

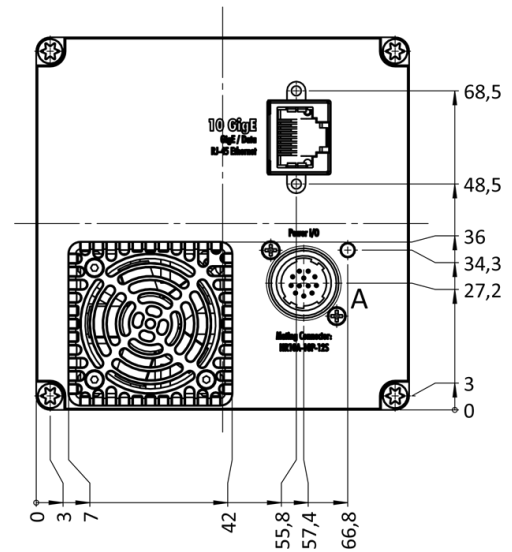


Technical Drawing

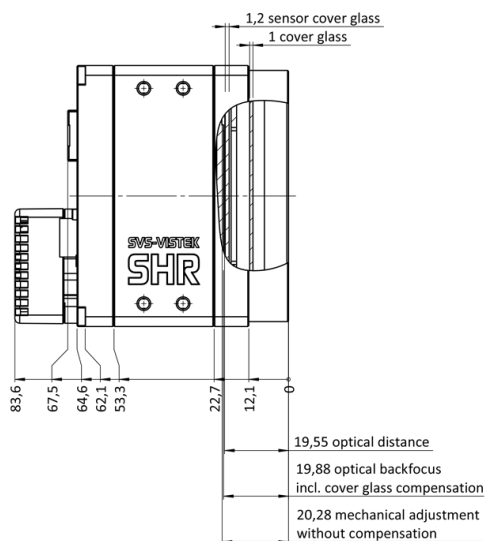
front



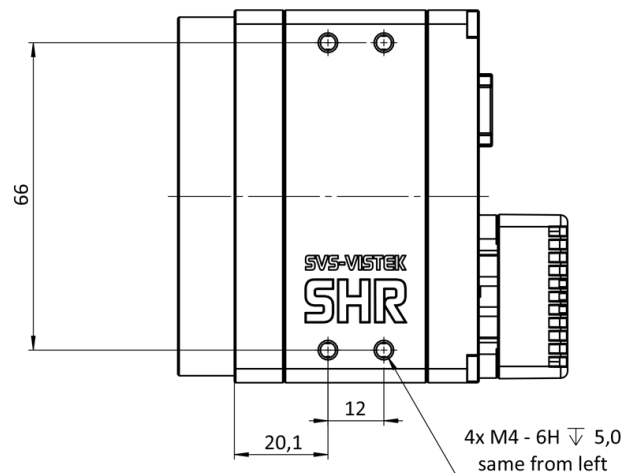
back



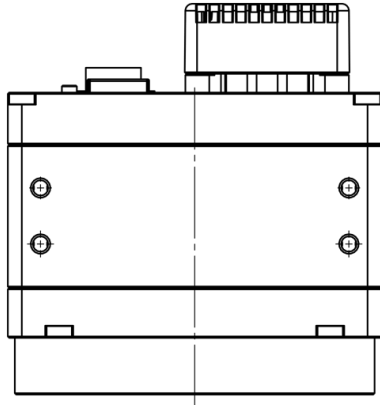
cross section



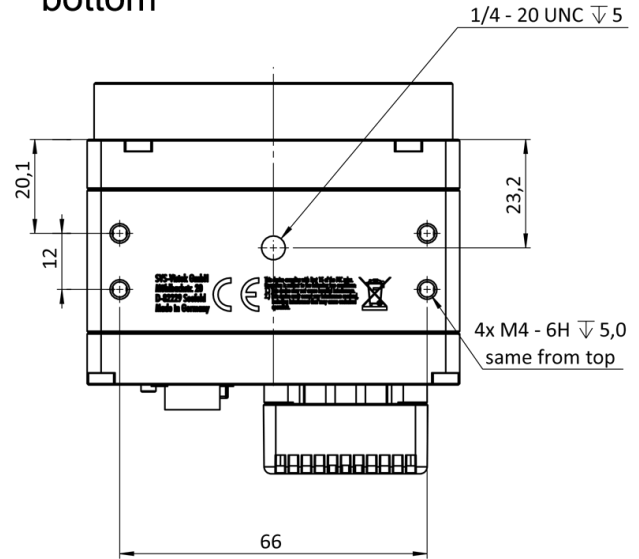
right side



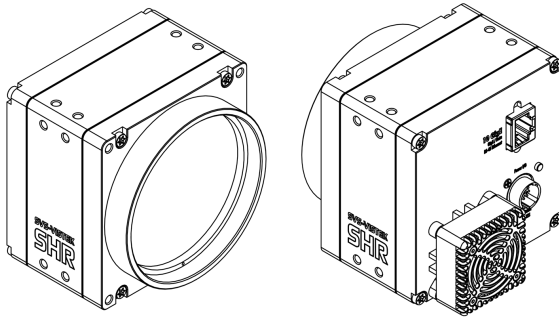
top



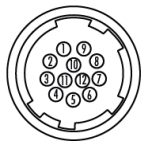
bottom



3D



I/O pin assignment

Hirose 12 Pin 	1	VIN -	(GND)	7	OUT 1	(open drain)
	2	VIN +	(10 V to 25 V DC)	8	OUT 2	(open drain)
	3	IN 4	(RXD RS232)	9	IN 3 +	(opto In +)
	4	OUT 4	(TXD RS232)	10	IN 3 -	(opto In -)
	5	IN 1	(0-24V)	11	OUT 3	(open drain)
	6	IN 2	(0-24V)	12	OUT 0	(open drain)